

ACTIVE MATRIX SUBSTRATE

100: GLASS SUBSTRATE

101: PIXEL MATRIX CIRCUIT

102: SCAN LINE DRIVE CIRCUIT

103; SIGNAL LINE DRIVE CIRCUIT

110 : PIXEL TFT

III ; PIXEL ELECTRODE

112: CAPACITOR

121; SCAN LINE

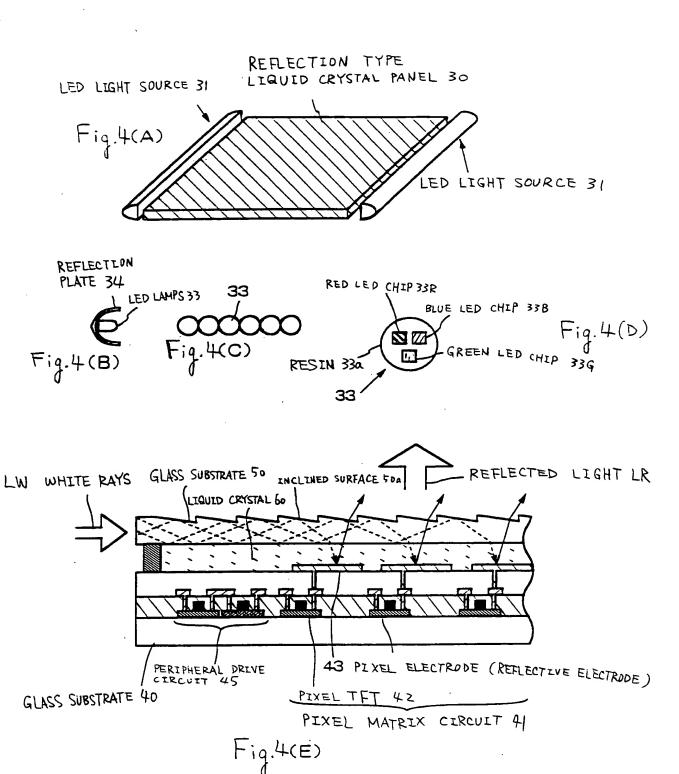
122: SIGNAL LINE

130: FPC

131; IC CHIP

132: IC CHIP

200 : COUNTER SUBSTRATE



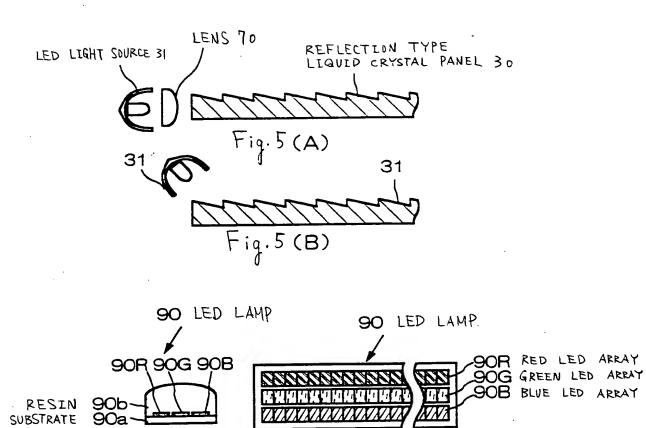


Fig. 6 (B)

Fig. 6 (A)

